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CU Term: Spring 2020 When: TR
1:30-3pm Where: Virtual Session
Instructor: Tania Khanna (Levine 262)
(seas: taniak) (office hours: WF
2:30-4pm and by appointment) TA:
Yuanlong Xiao (seas: ylxiao) Office
hours: TuTh 3-5pm in Virtual Session TA:
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References: CMOS Digital Integrated Circuits: Analysis and Design S-M. Kang and Y. Leblebici 3rd edition . CMOS VLSI Design - A Circuits and Systems Perspective N. H. Weste and D. Harris 4th edition . Midterm: Mon 10/31/11 in class.

EEEC116 - Fall 2011

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